

## 1 Secure LSI device



FIG.2

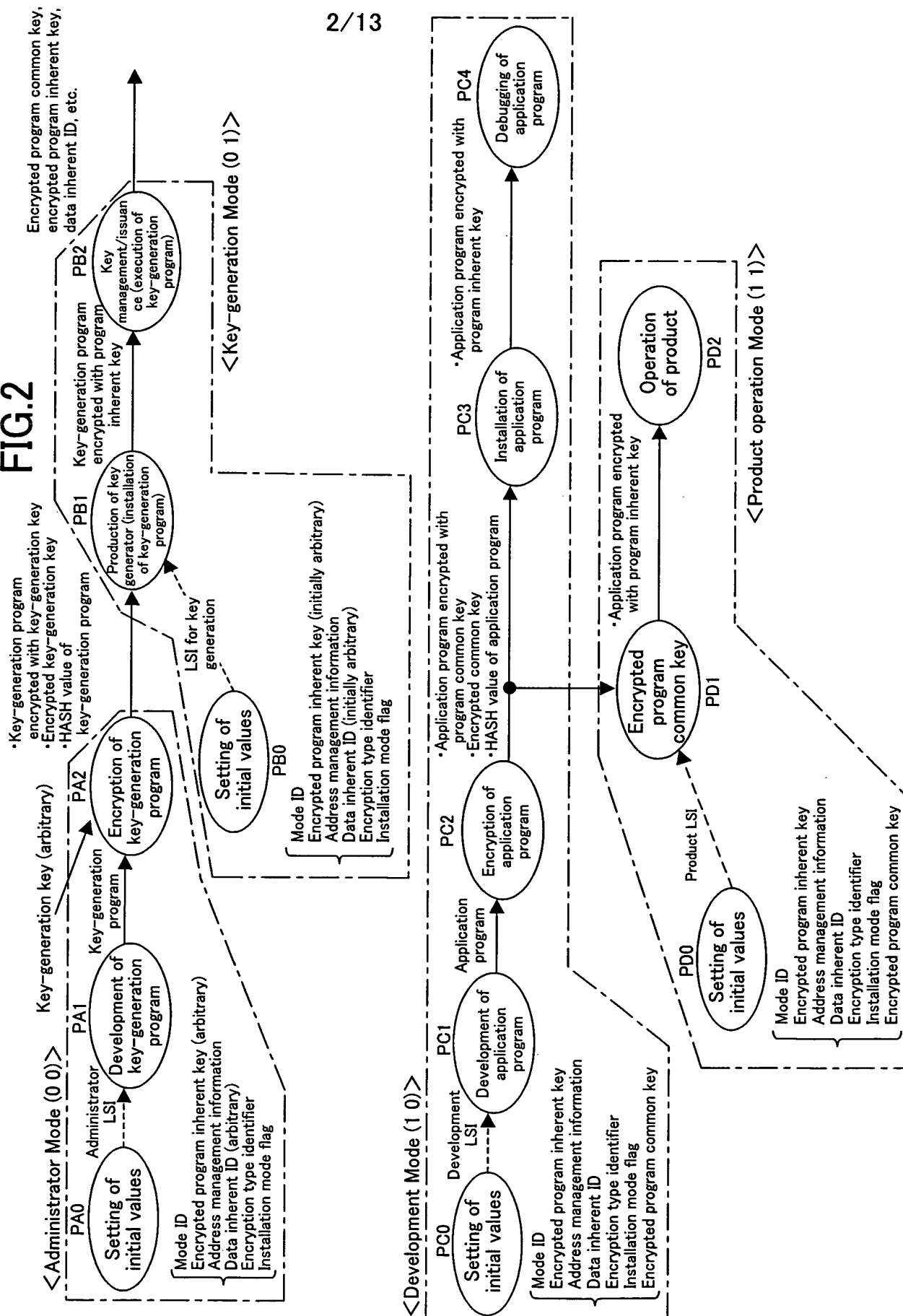


FIG.3

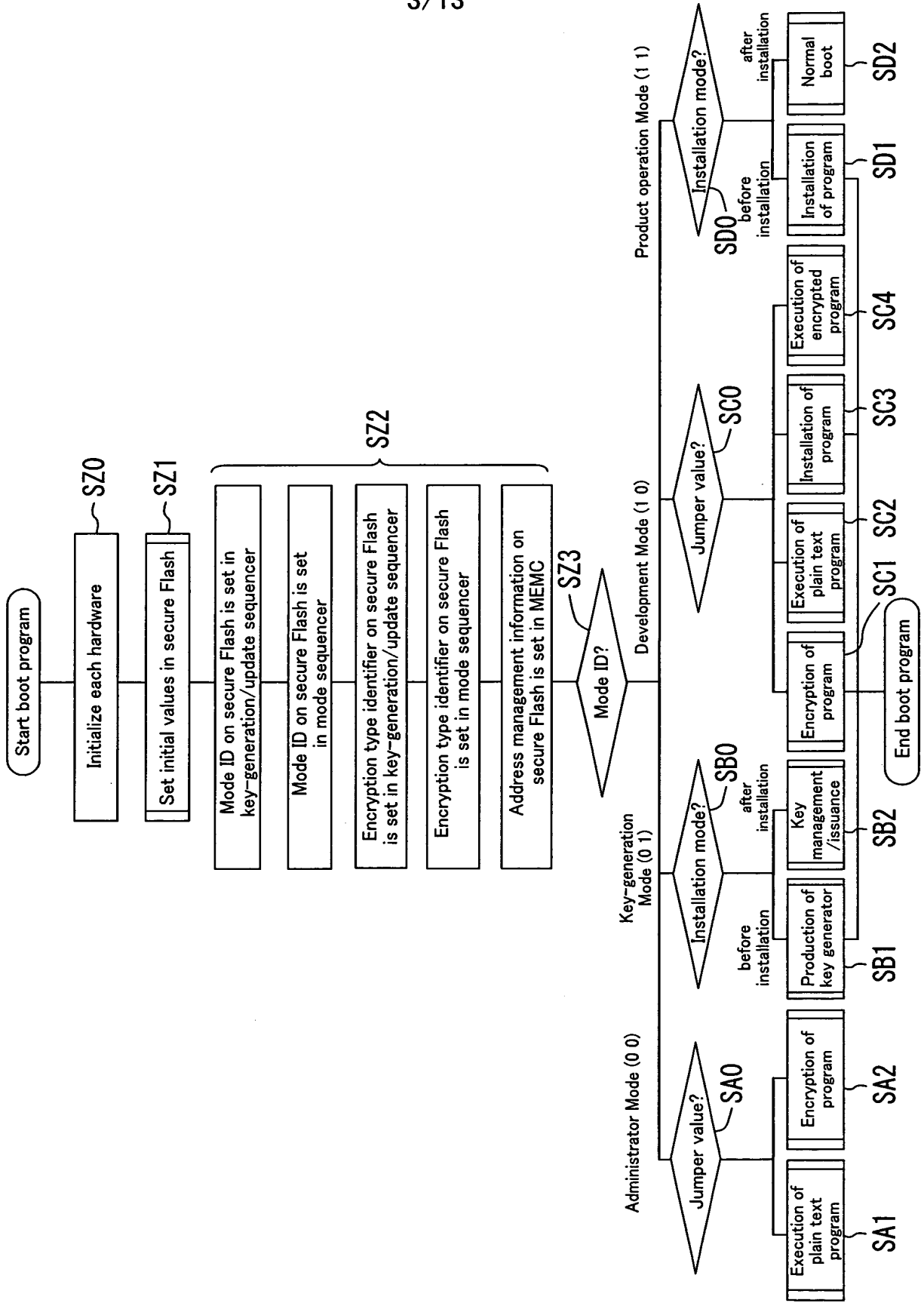
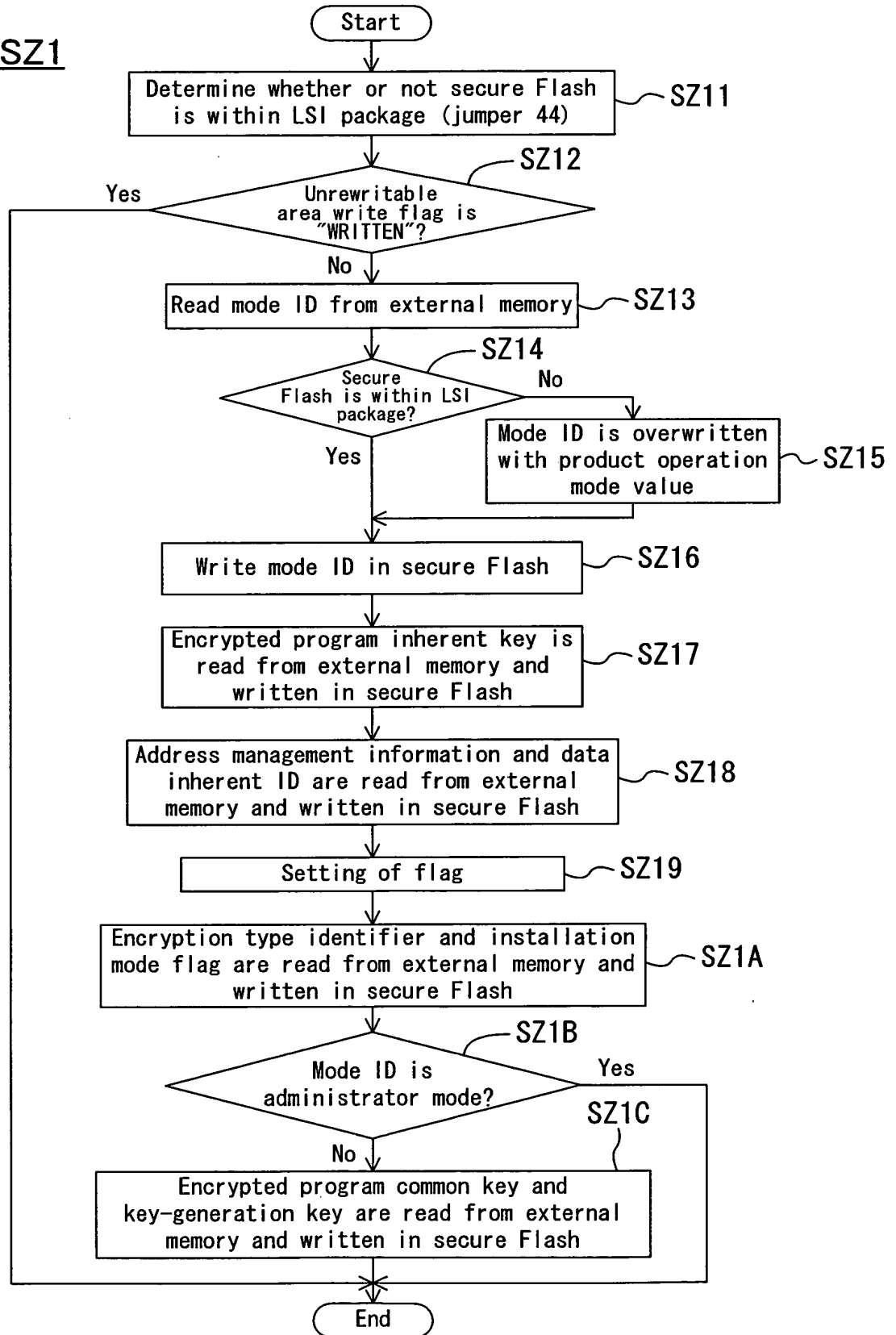


FIG.4

SZ1

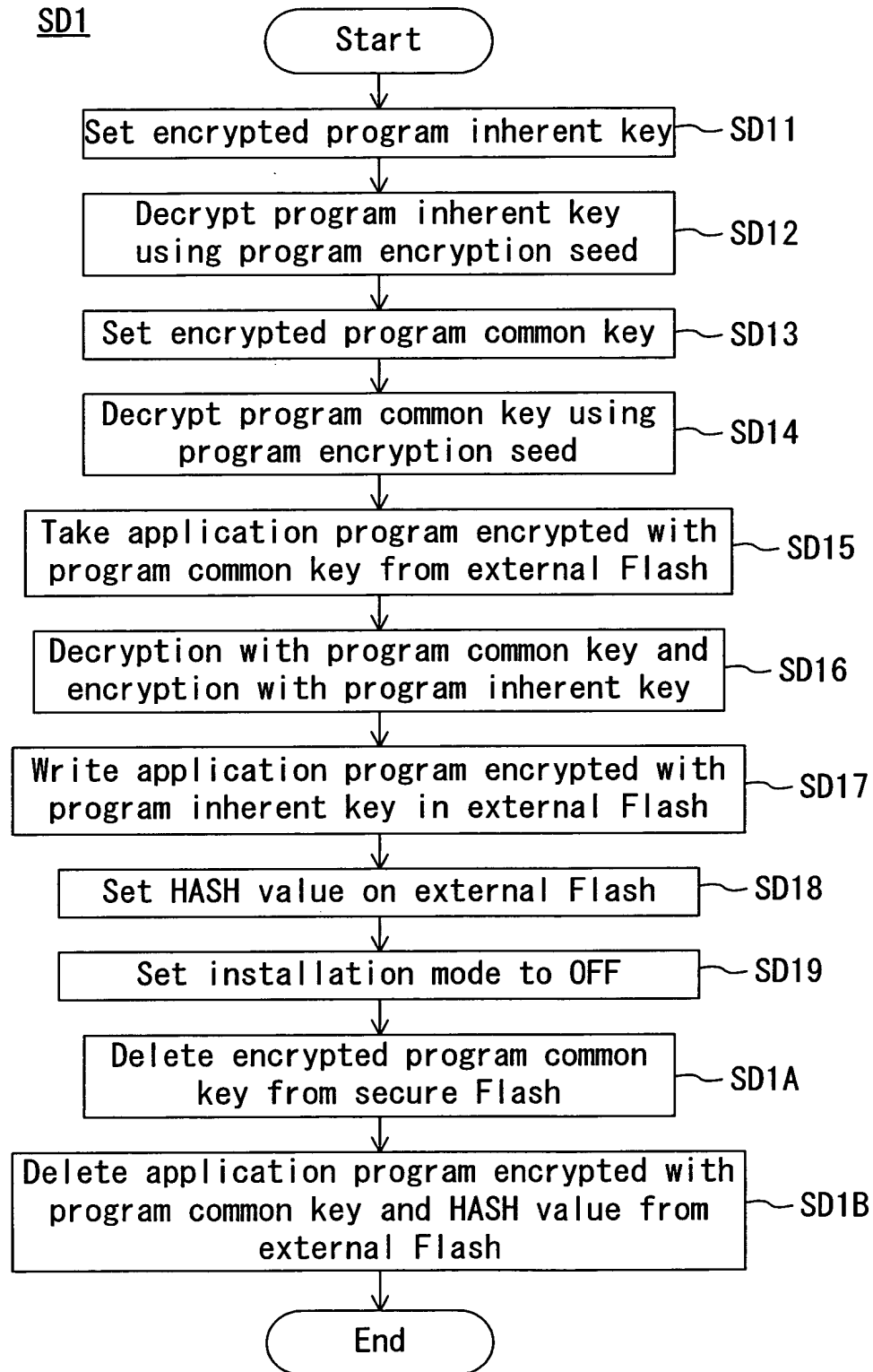
**FIG.5**SD1

FIG.6

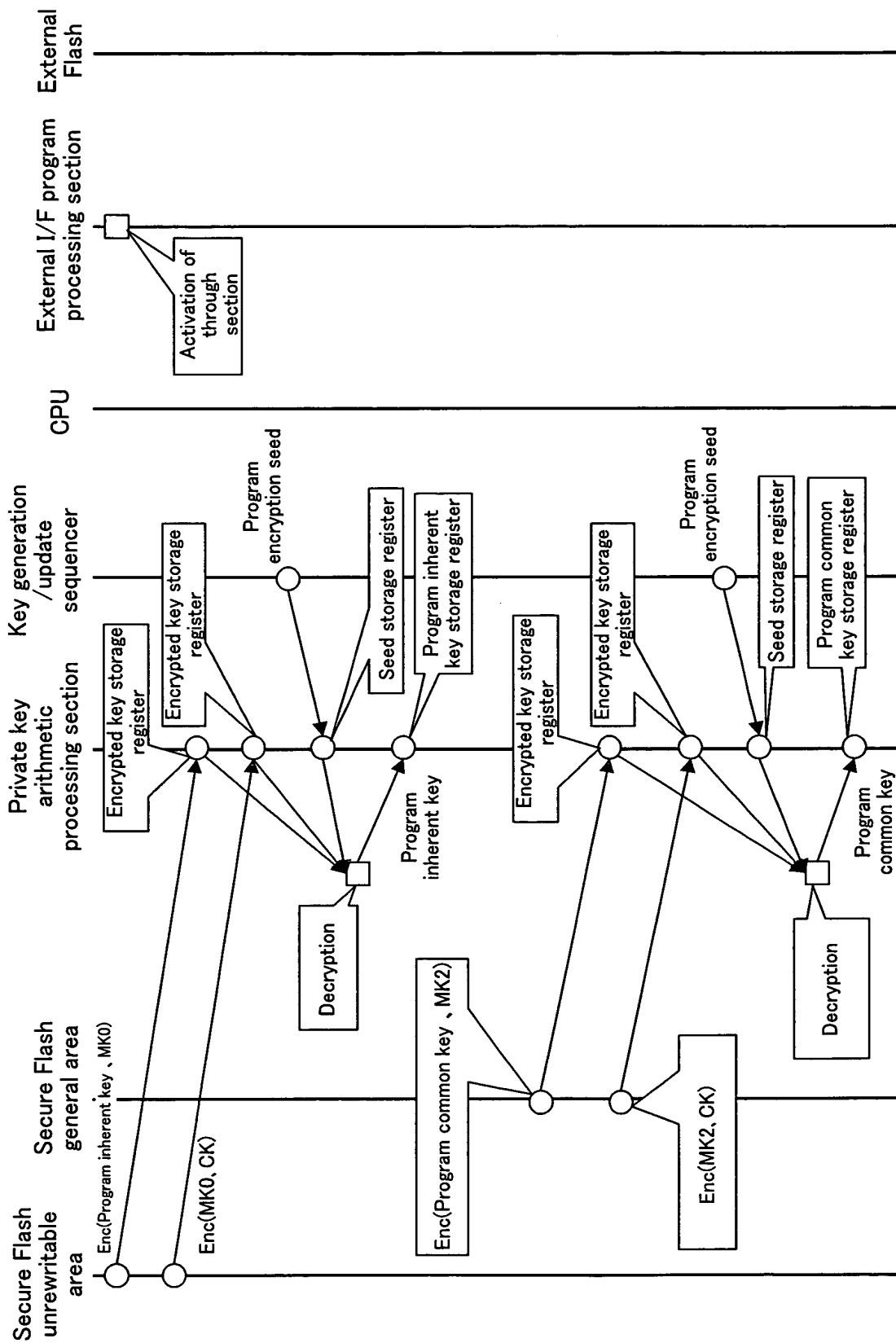


FIG.7

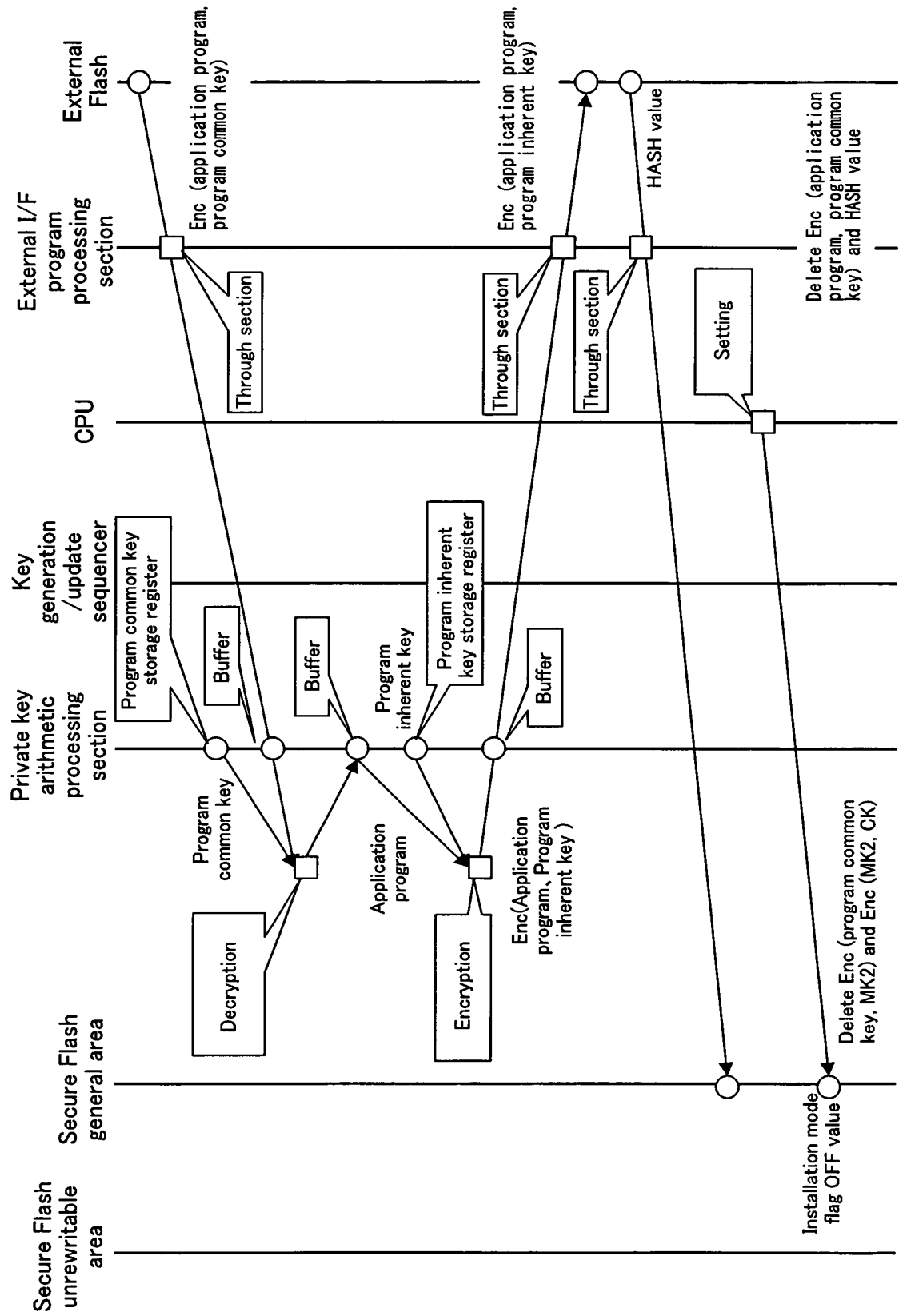


FIG.8

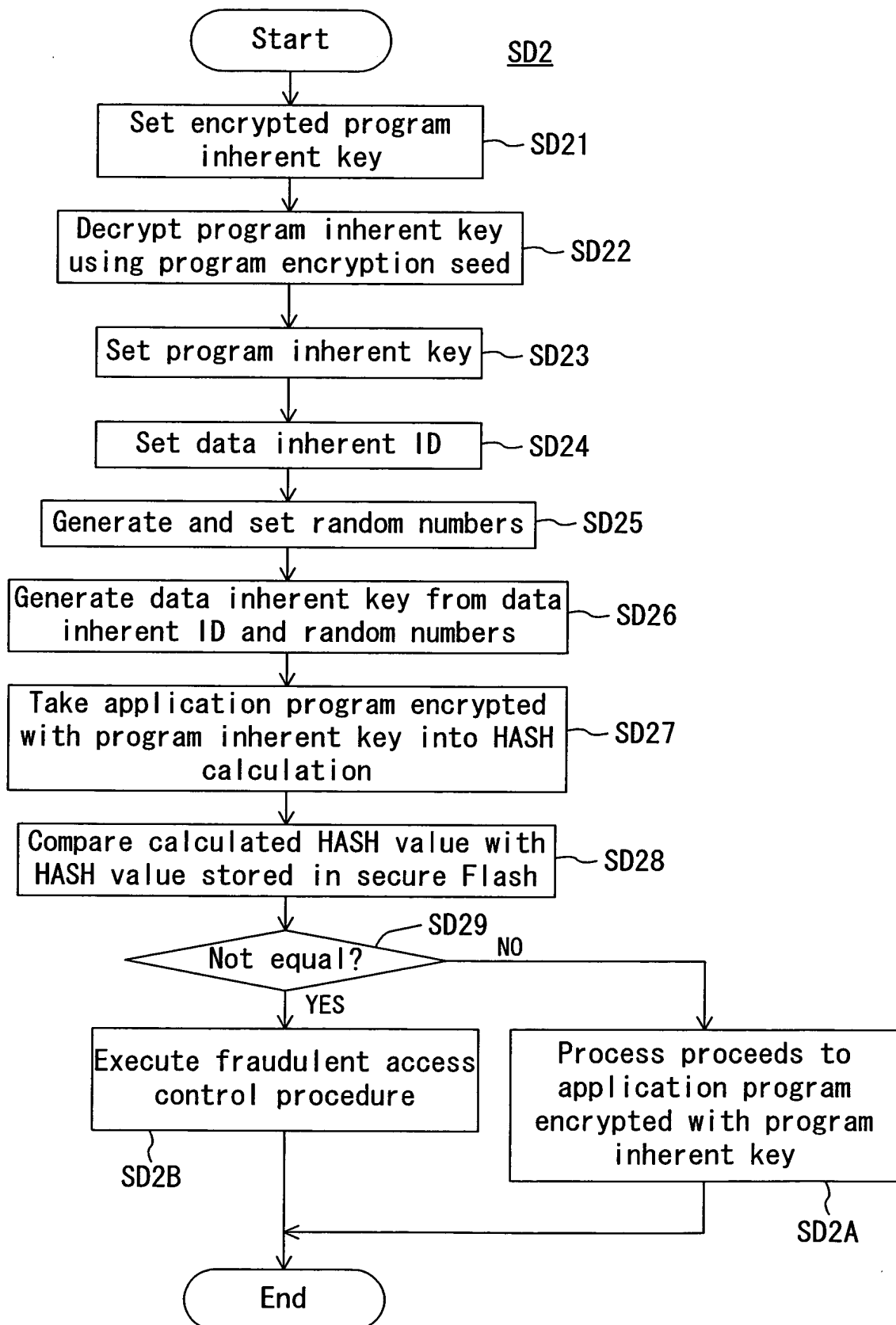




FIG.9

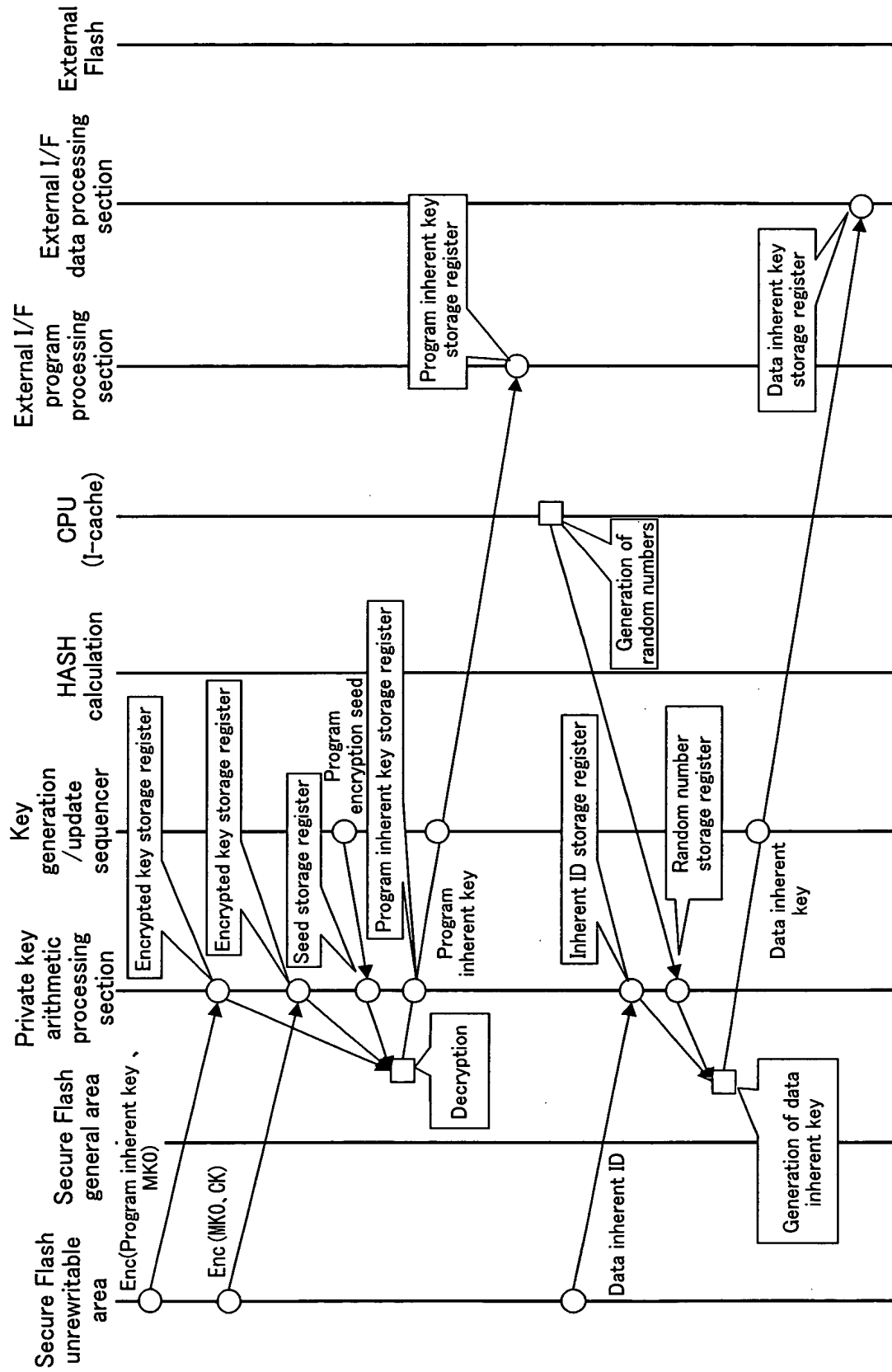


FIG.10

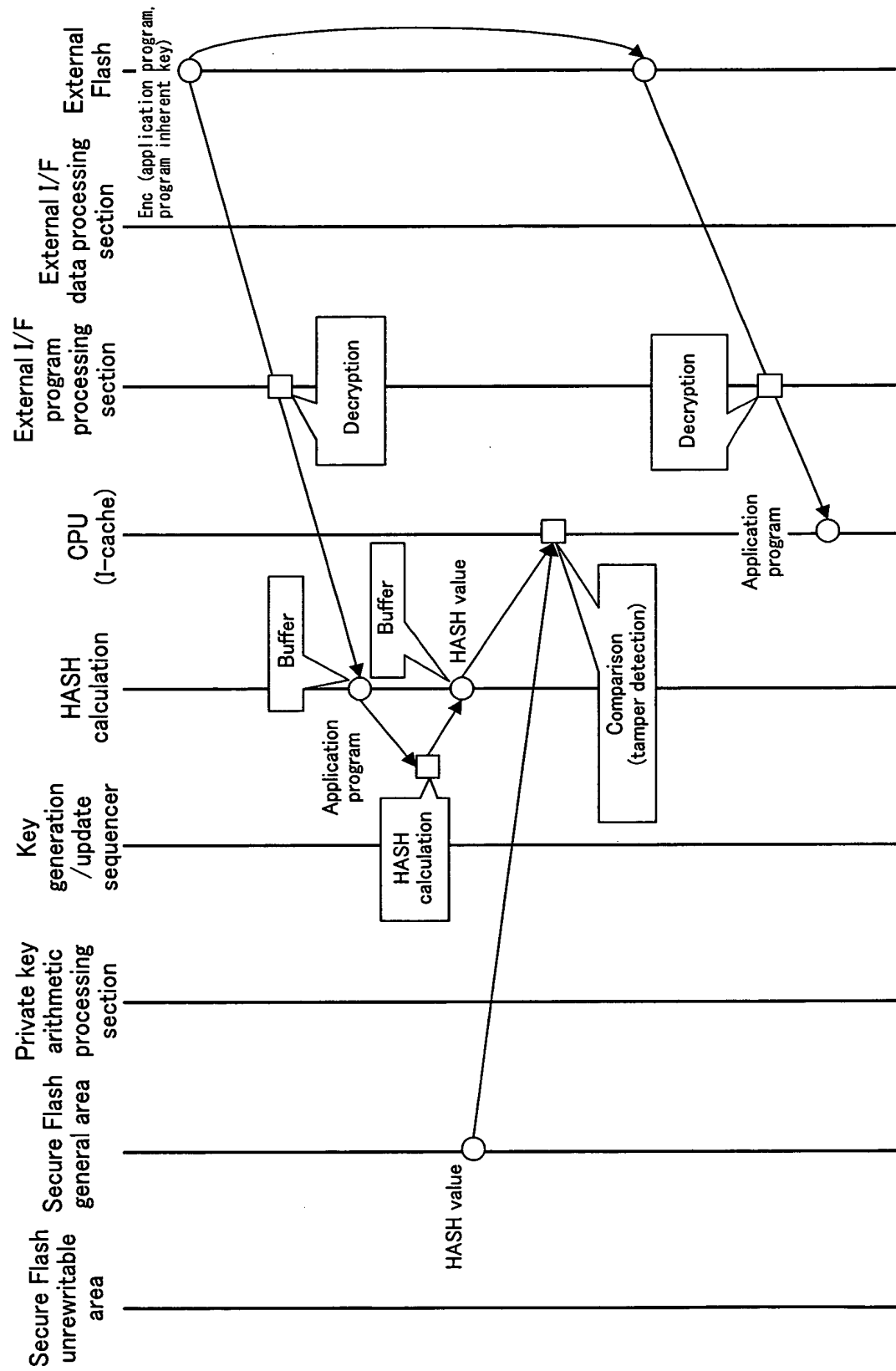


FIG.11

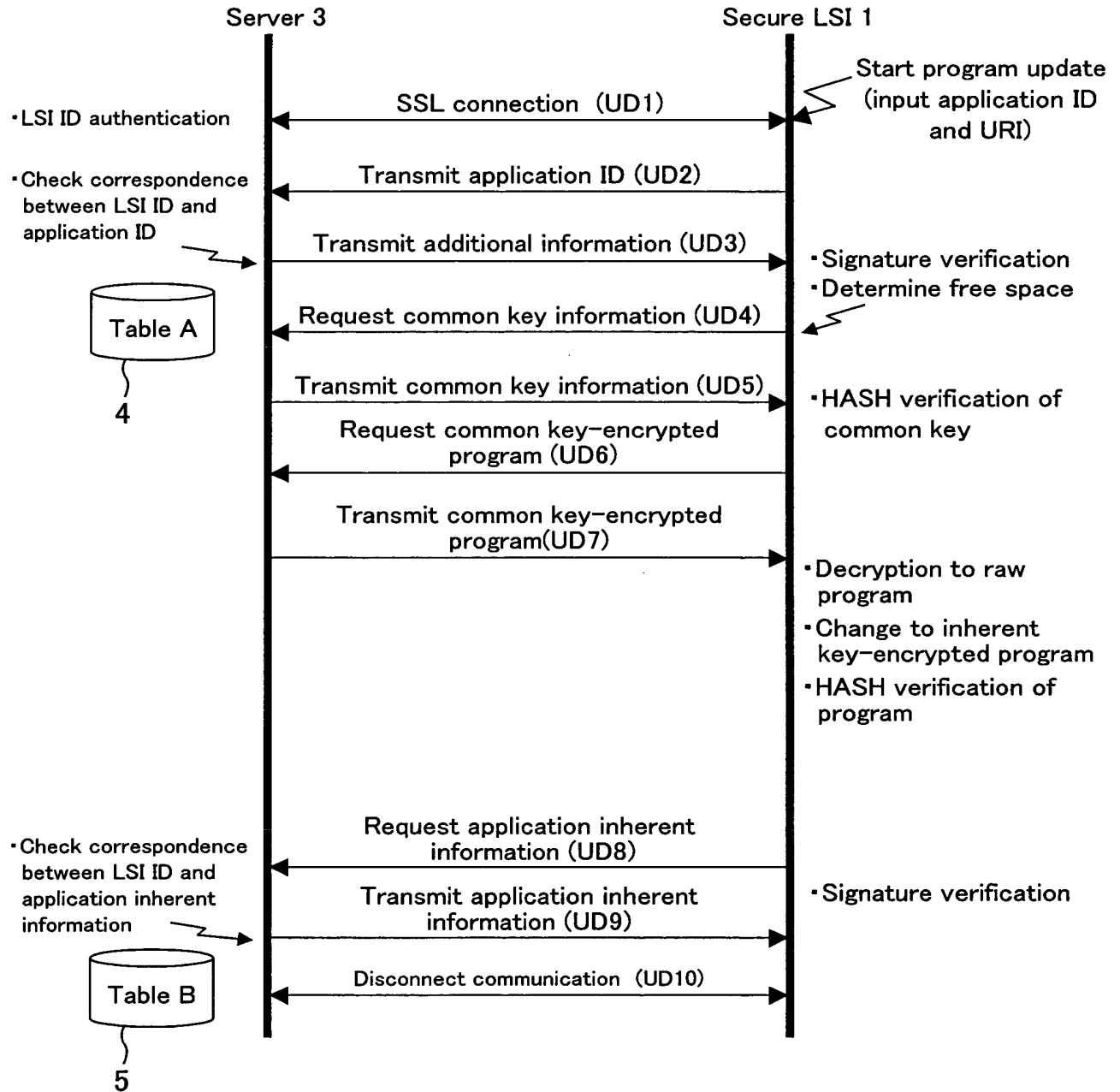


FIG.12

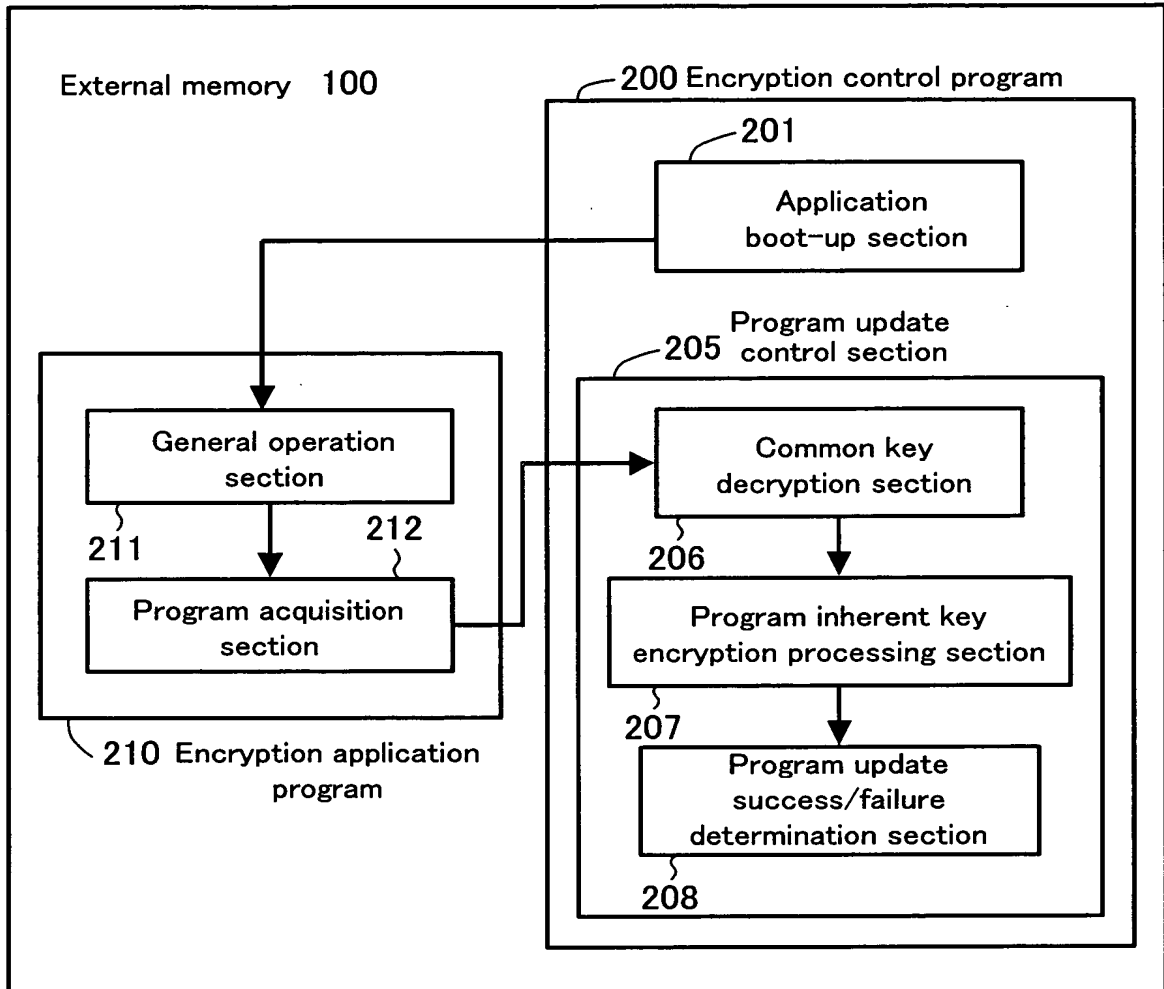


FIG.13

